

ISDN U-INTERFACE TRANSFORMERS

FEATURES

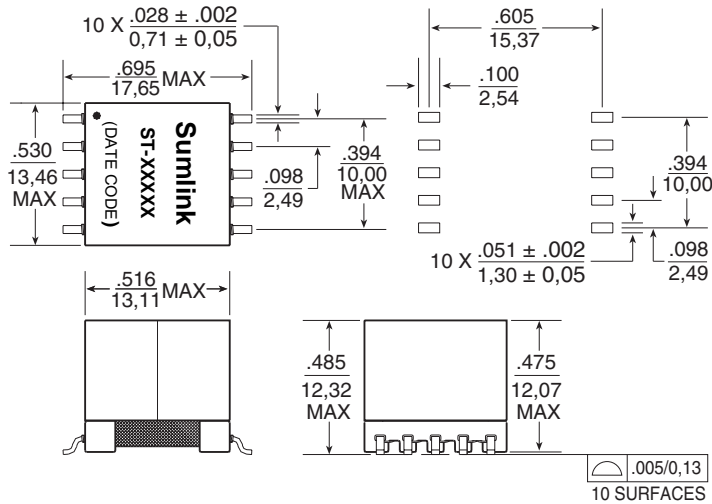
- For Applications in 2B1Q and 4B3T Coding
- Meets ANSI T1.601-1992 and ETSI ETR 080 standards for longitudinal balance, linearity and return loss
- Small compact footprint and minimum Isolation: 1500 Vrms



Electrical Specifications @ 25°C

Part Number	Turns Ratio Line:Chip (±2%)	Line Side Pins	OCL Line side (mH)	DCR Line Side (Ω MAX)	DCR Chip Side (Ω MAX)	Idc (mA)	Return Loss (dB MIN)		Longitudinal Balance (dB MIN)			Sche.
							10-25 KHz	15-50 KHz	281-25 KHz	15-50 KHz	20-150 KHz	
ST-84031	1.65CS:1CT	(1-5)	13-18	14.00	7.00	50	20	-	55	-	-	A
ST-84032	1.25CS:1CT	(1-5)	11.9-16.1	6.40	4.40	50	-	16	-	-	40	A
ST-84033	1.25CS:1CT	(1-5)	25.5-30.5	15.66	10.65	40	20	-	55	-	-	B
ST-84043	2CS:1CS	(1-5)	13.5-16.5	7.00	3.50	50	20	-	55	-	-	C
ST-84067	1.32CS:1	(1-5)	7.5-8.3	3.20	2.50	55	-	16	-	40	-	D

Mechanicals



Transformer Selection

Company	Chip	Transformer
Motorola	MC145572	ST-84033
Infineon	PEB 2091	ST-84031
	PEB 8091/8191	ST-84031
	PEB 24902/24911	ST-84031
	PSB 21910/21911	ST-84031
	PEB 2491	ST-84032
	PEB 20901	ST-84067
Alcatel	MTC 20276	ST-84043

Dimensions: $\frac{\text{Inches}}{\text{mm}}$
Unless otherwise specified, all tolerances are $\pm \frac{.010}{0,25}$

Schematics

