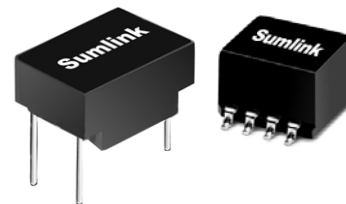


TRANSFORMERS FOR ECHELON[®] NEURON[®] ICs

FEATURES

- Developed for Use with 3120[™] & 3150[™] Chips
Produced by Motorola and Toshiba
- For 1.25 Mbps twisted pair applications
- Provides 1500 Vrms isolation for 1 minute



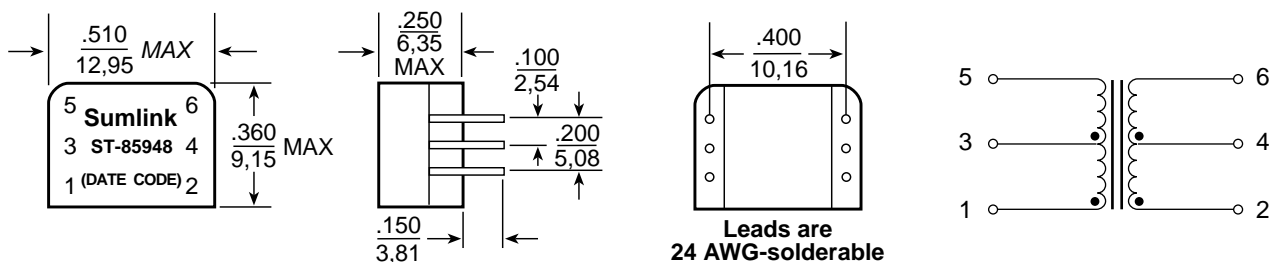
Electrical Specifications @ 25°C — Operating Temperature -40°C to 85°C

Part Number	Turns Ratio ¹ ±2%	Primary Inductance (mH)	DCR Primary (Ω) MAX	DCR Secondary (Ω) MAX	Primary Pins (Network Side)	Package
ST-85848	1CS:1CS	3.5-6.5	1.0	1.0	8-5	Surface Mount
ST-85948	1CT:1CT	3.5-6.5	1.0	1.0	2-6	Through Hole

Mechanicals

Schematics

ST-85948



ST-85848

